

Patent

Attorney Docket No.: Intel 2207/10122
Assignee: Intel Corporation



Response Under 37 C.F.R. §1.116
Expediting Procedure
Examining Group 2183

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Sailesh B. KOTTAPALLI
SERIAL NO. : 09/753,764
FILED : December 29, 2000
FOR : METHOD AND APPARATUS FOR REDUCING
COMPONENTS NECESSARY FOR INSTRUCTION POINTER
GENERATION IN A SIMULTANEOUS MULTITHREADED
PROCESSOR
GROUP ART UNIT : 2183
EXAMINER : David J. HUISMAN
M/S: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail and deposited with the U.S. Postal Service and addressed to: M/S: AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Dated: September 20, 2004

Signature Barbara Vance
Barbara Vance

**AMENDMENT UNDER 37 C.F.R. §1.116 AND
REQUEST FOR RECONSIDERATION**

Dear Sir:

The following were sent in response to the office action mailed on July 20, 2004.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 8 of this paper and include both replacement sheets and annotated sheets showing changes.

Remarks/Arguments begin on page 9 of this paper.

An Appendix with amended drawing figures is attached following page 11 of this paper.

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